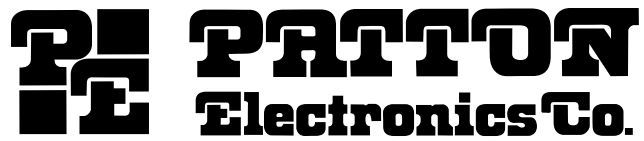


Doc #: 124001UA  
Part #: 07M3080-A



DIGITAL SHARING DEVICE

**3080/V24**

(CTS DSD-8)

INSTALLATION AND OPERATIONS MANUAL

August 23, 2000



An ISO-9001  
Certified Company

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Anhang 2 zur Anlage 1 zur AmtsblVfg 243/1991

Bescheinigung des Herstellers / Importeurs

Hiermit wird bescheinigt, daß der/die/das

8 Port Digital Sharing Device, 3080/V24 (CTS DSD-8), 124000

(Gerät, Typ, Bezeichnung)

in Übereinstimmung mit den Bestimmungen der BMPT-AmtsblVfg 243/1991 funkentstört ist. Der vorschriftsmäßige Betrieb mancher Geräte (z.B. Meßsender) kann allerdings gewissen Einschränkungen unterliegen. Beachten Sie deshalb die Hinweise in der Bedienungsanleitung.

Dem Zentralamt für Zulassungen im Fernmeldewesen wurde das Inverkehrbringen dieses Gerätes angezeigt und die Berechtigung zur Überprüfung der Serie auf die Einhaltung der Bestimmungen eingeräumt.

**Patton Electronics Co.**

7622 Rickenbacker Drive

**Gaithersburg, Maryland, USA 20879**

(Name und Anschrift des Herstellers / Importeurs)

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# CHAPTER 1 - OPERATION

The Patton 3080/V24 (CTS DSD-8) is a network enhancement accessory intended for high speed synchronous or asynchronous Modem, or port sharing applications. The 3080/V24 (CTS DSD-8) allows for immediate expansion of a system without the requirement of additional communication lines. The 3080/V24 (CTS DSD-8) allows any combination of up to eight DCE or DTE devices to share a single DCE or DTE high speed link in a point-to-point or multi-point polled network.

## Channel Clocking

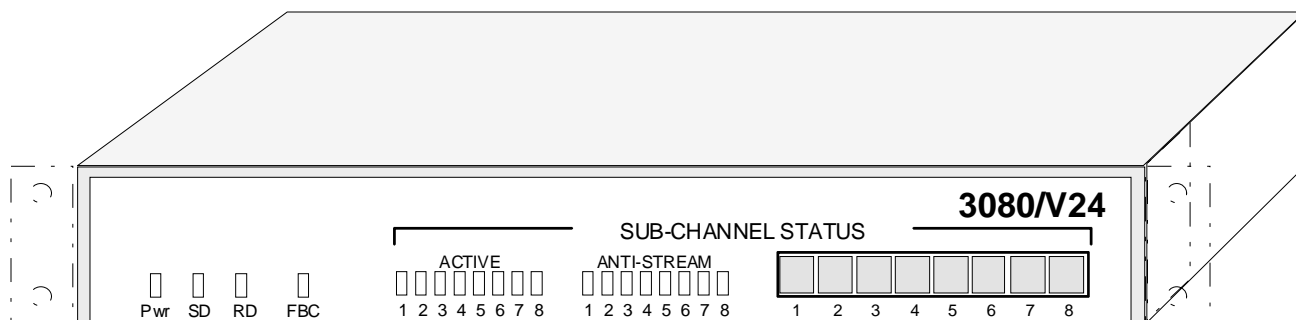
The Patton 3080/V24 (CTS DSD-8) is protocol transparent and operates in synchronous and asynchronous environments at data rates up to 76,800bps, depending on cable length and attached equipment. Clocking can be derived from the Master Port clock, (pins 15 or 24 for TXC and 17 for RXC), the internal clock generator or any Sub-Channel 1 through 6 (pins 15 or 24 for TXC and 17 for RXC).

## Fallback Clocking

In the event of loss of an externally provided clock or optionally Sub-Channel 1 DCD becoming inactive, the 3080/V24 (CTS DSD-8) has the ability to automatically fall back to the predetermined internal baud rate clock or the Sub-Channel 6 RX Clock (pin 17).

## Channel Interface

The 3080/V24 (CTS DSD-8) has nine DB-25 female connectors located on the rear of the unit. Each connector is pinned with EIA RS-232, using RS-423 electrical characteristics. This enables extended cable driving distances, which often bypasses the need for line drivers. Each port is DCE/DTE selectable, which eliminates the need for a crossover cable. DSR or DTR can additionally be forced to the active state individually on each Sub-Channel by Jumper control.



## Anti-streaming

A typical problem often encountered is a “*STREAMING*” remote terminal. The streaming problem can tie up an entire circuit until the offending device has recovered or is powered down. A streaming condition occurs when a Sub-Channel remains active, disrupting the polling sequence. The 3080/V24 (CTS DSD-8) provides two user selectable modes of controlling a streaming condition, an automatic anti-streaming Abort Timer with eight selectable block lengths and a setting to disable anti-streaming for large data block transfers, and a manual Operator Control mode (front panel push button switch).

**Automatic Anti-streaming** - Automatic Anti-streaming will block the asserted control signal from the Sub-Channel Port, releasing the 3080/V24 (CTS DSD-8) to accept requests from any other Sub-Channel that has not been locked out due to a streaming condition. When a Sub-Channel is locked out for streaming, a visual indication, by Sub-Channel, is provided on the front panel. *Any data sent to the Master Port by the streaming device will be lost, but data will still be broadcast to locked out Sub-Channels.* Once a Sub-Channel is locked out due to streaming, the Sub-Channel will remain locked out until the attached device removes the streaming condition. When configured for “Switch on RTS” Mode, if RTS is held active, RTS must become inactive. When configured for “Switch on Data” Mode, if continuous data transitions have triggered the Anti-Stream logic, the transitions must stop before the DSD-8 will clear the Anti-Stream logic to that Sub-Channel. Removing the Sub-Channel via the front panel disable switch will not clear the Anti-stream logic for a Sub-Channel once it has been activated.

**Manual Anti-Stream** - If manual Anti-Stream protection is chosen, a streaming condition caused by one of the attached terminals can be quickly corrected by the 3080/V24 (CTS DSD-8) via the associated front panel locking switches. A switch is provided for each Sub-Channel and permits the rapid removal of a streaming terminal without having to disconnect any cables or power down the offending terminal. Terminals may be selectively removed for self-test and maintenance without affecting the remaining Sub-Channels. Once the streaming condition has been corrected, the front panel switch is simply depressed to the locked (Enable) position (Green indicator **ON**) to re-establish normal operation. Removing a Sub-Channel with the front panel switch will block data from the Sub-Channel going to the Master Port but will not block data going from the Master Port to the Sub-Channel.

## Channel Selection Modes

The 3080/V24 (CTS DSD-8) provides two selectable Sub-Channel service modes of operation, the Scanning Mode and Priority Mode. Depending on system requirements, either mode may be selected by internal DIP switch settings. Both modes switch on activity from the Sub-Channel.

In the Scanning Mode the 3080/V24 (CTS DSD-8) scans each Sub-Channel, in sequence, beginning with Sub-Channel 1. This rotational sequence is repeated continually with each attached Sub-Channel having equal access to the communications link. When data or control lines from a Sub-Channel become active that Sub-Channel is switched through to the master port by the 3080/V24 (CTS DSD-8). All remaining Sub-Channels are locked out until the first device becomes inactive. When the Sub-Channel device becomes inactive, the 3080/V24 (CTS DSD-8) will resume scanning the Sub-Channels for another active signal.

When configured for Priority Mode operation, the 3080/V24 (CTS DSD-8) monitors all Sub-Channels simultaneously with Sub-Channel 1 having the highest priority. When a Sub-Channel becomes inactive, the 3080/V24 (CTS DSD-8) will automatically default to the highest priority (lowest number) Sub-Channel with activity.

Contention for the Master port is accomplished by asserting RTS (Request to Send) if the Sub-Port is configured as a DCE, DCD (Data Carrier Detect) if the Sub-Port is configured as a DTE or Data Transitions from the attached Sub-Channel devices in either configuration. The active interface lead RTS or DCD can be selected on an individual basis for each Sub-channel, selection of contention mode, "*Data Transitions*" or "*Interface Lead*" activation is selected in Sub-channel groups. Sub-channels 1 thru 4 are the first group and Sub-channels 5 thru 8 are the second group. All Sub-channels in a group will be set to the same contention mode, either "*Data Transitions*" or "*Interface Lead*" activation. Once a Sub-Channel asserts an active control signal, that control signal will be passed through to the Master Port, depending on DTE / DCE configuration of the Sub-Channel and Master Port. This control will be passed without delays. If the Master Port is configured as a DTE the signal will be passed as RTS. The CTS returned to the Master will have an optional delay added before the signal is returned to the Sub-Channel port.

Port Enable / Disable, front panel switches must be pushed in (**GREEN** indication) for a Sub-Channel to access the Master Port regardless of the mode of operation selected.

### Channel Tail Circuits

A buffer is built into the unit for tail circuit (DCE to DCE) applications. An 8 bit centered ring buffer is used to correct the clock phasing errors generated between the Modem on the Master Port and the Sub-Channel port for the data transferred from the Sub-Channel to the Master Port. If the unit is operated in an asynchronous environment the buffer must be bypassed. This is accomplished by moving the **SYNC/ASYNC** switch to **ASYNC**.

## Interface Connections

All connections are made via industry standard EIA RS-232 DB-25 female connectors located on the rear of the unit. The following interface leads are implemented, Chassis (1), TXD (2), RXD (3), RTS (4), CTS (5), DSR (6), Sig Gnd (7), DCD (8), Plus Unreg (9), Minus Unreg (10), TXC (15), RXC (17), DTR (20), XTXC (24). Pins 9 & 10 can be disabled by Jumpers on the PCB.

## Front Panel LEDs

Front panel LEDs are provided to indicate power is applied, RXD and TXD activity from the Master Port, Sub-Channel activity of each individual Port, Anti-Stream condition of each Sub-Channel and an indication of clock fallback condition.

## Channel Enable / Disable Switches

Front panel switches allow operator intervention to remove an individual Sub-Channel from accessing the Master Port. Positive latching type switches are provided for each Sub-Channel port for isolating or removing a streaming terminal. The Sub-Channel is activated by pushing the switch until it is in the “**IN**” position. The switch will indicate **GREEN** in color. To disable a Sub-Channel push the switch until it locks in the “**OUT**” position, the switch will indicate **BLACK** in color. Disabling a Sub-Channel with the front panel does not prevent the Sub-Channel from receiving data from the Master Port only from sending data to the Master Port.

## Power Supply

A linear power supply is located internally, an external 110/220VA switch is located on the rear of the unit. If chassis ground and signal ground need to be tied together this can also be accomplished by switch selection. The unit is Rackmountable for 19" (or optional 23") cabinets, by using the included Rackmount Kit. Safety approvals granted are UL, CSA and TÜV. Emission approvals for FCC Class A and VFG243 have also been granted.



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**Caution, Disconnect the POWER Before Removing The Cover**  
**Vorsicht, Befor Deckung Abnehmen Mach Strom Zu.**

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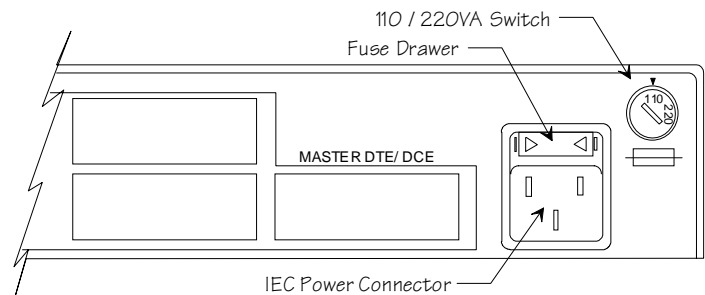


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## CHAPTER 2 - SETUP AND INSTALLATION

### Power Connection

Before connecting the 3080/V24 (CTS DSD-8) to a AC power source the top cover must be installed and secured with the supplied #8-32 screws. The unit is supplied with a 110/220VA voltage switch, turn the switch with a coin or screw driver to the appropriate voltage for your country. If the unit is set to 110V insure that the fuses are 0.16A Slow Blow (Little Fuse 218.160). For 220V operation use 0.08A (Little Fuse 218.080) Slow Blow fuses. EXAMPLE: United States of America; set to 110VA. The unit is supplied with a IEC power connector next to the voltage select switch, plug the power cord into the connector until it is firmly seated. You may now connect the power cord into your AC outlet.



### Factory Configuration Switch Settings

The 3080/V24 (CTS DSD-8) is configured prior to shipment with the switches set to the following default positions:

**Switch 27** - 6 **OFF**, 1,2,3,4 and 5 **ON**.

TX Clock Source = Master, pin 15  
 Chassis Ground not connected to Signal Ground

**Switch 28** - 4 and 5 **OFF**, 1,2,3 and 6 **ON**.

RX Clock Source = Master  
 Anti-Stream timer = 16K bits

**Switch 29** - 1, 2 and 4 to **OFF**, 3,5 and 6 **ON**.

No Data Time Out = 2048 bits  
 Anti-Stream = Enabled  
 Broadcast Data to Sub-Channels  
 Sub-Channel Activity Indicated by Interface Control (RTS/DCD)

**Switch 30** - 1 and 2 **OFF**, 3, 4, 5 and 6 **ON**

Internal Baud Rate = 9600, Normal  
Fallback Clock Source = Data Transitions  
Fallback Clock = Disabled

**Switch 31** - 2 and 3 to **OFF**, 1,4,5 and 6 to **ON**

Sync Mode  
Contention Mode = Scan  
CTS Delay = 1 mS

If the system application requires one or more of the default settings to be changed, it will be necessary to remove the top cover of the enclosure to access and change the DIP switches located on the printed circuit board.

## Disassembly

Remove the top cover by removing the phillips head screws located on the left and right sides of the 3080/V24 (CTS DSD-8). DTE/DCE switches SW9 through SW26, interface Jumpers J4 through J14 and configuration switches SW27 through SW31 are located on the PCB as indicated on the strapping guide in the Appendix of this manual. After the switch selection activity is completed, ***re-install the top cover BEFORE connecting to a AC power source.***

## Installation

Select an appropriate location accessible to and within six feet of an AC power outlet, the outlet must have a ground pin receptacle for product warranty. The DCE-to-DTE cabling between each attached device and the 3080/V24 (CTS DSD-8) should be "Straight Through", shielded and terminated with male connectors. Sub-Channels are marked PORT 1 through PORT 8, the Master Port is marked, MASTER DCE/DTE. If any terminal has a priority service mode, ensure it is connected to the port connector designated "PORT 1" on the rear panel of the 3080/V24 (CTS DSD-8). Secure other terminals or Modems to be serviced to the remaining "PORT" connectors. Connect the MODEM or TERMINAL to the connector designated "MASTER DTE/DCE".


### Equipment Grounding (SW27-6) (default OFF)

Switch SW27-6 provides for grounding interconnection in those systems requiring a connection between (Frame Ground) and (Signal Ground). Connect **ONLY** if required.

### CTS Delay (SW31-3,4,5)

If a CTS delay is desired, set SW31-3, 4 and 5 to the required value as indicated below.


SW31-3	SW31-4	SW31-5	Time
ON	ON	ON	No Delay
OFF	ON	ON	1nS
ON	OFF	ON	2nS
OFF	OFF	ON	4nS
ON	ON	OFF	9nS
OFF	ON	OFF	18nS
ON	OFF	OFF	36nS
OFF	OFF	OFF	72nS



### Anti-Streaming (SW28-4,5,6 and SW29-3)

The maximum data block size is user selectable via switch SW28- 4, 5 and 6. As shown below eight block sizes are provided to the user. To disable anti-streaming set SW29-3 to the **OFF** position. The maximum block size is normally defined at the time of installation.

SW28-4	SW28-5	SW28-6	CLOCKS
ON	ON	ON	1024
OFF	ON	ON	2048
ON	OFF	ON	4096
OFF	OFF	ON	16 K
ON	ON	OFF	64 K
OFF	ON	OFF	256 K
ON	OFF	OFF	1 MEG
OFF	OFF	OFF	2 MEG




### Receive Data Mode (SW29-4) (default OFF)

Setting switch SW29-4 to **ON** sets the DSD-8 into the Gated Mode. Only the Sub-Channel that is currently active will receive the data from the master port. Setting position 4 to **OFF** enables the Broadcast mode. Receive Data from the Master Port is sent to all Sub-Channels.

### Control Turn-Off Delay in "DATA" Switching Mode (SW29-1,2)

When configured to switch on DATA, switch SW29-1 and 2 set the Sub-Channel release time in clocks. The value selected is the number of clocks at the current baud rate that must occur without any data transitions on the Sub-Channel to indicate that Sub-Channel has completed its transmission and the 3080/V24 (CTS DSD-8) can resume servicing the other Sub-Channels. The following table should be used to set this time period.

SW29-1	SW29-2	CLOCKS
ON	ON	16
ON	OFF	64
OFF	ON	256
OFF	OFF	2048




### Switch on Data / Control (SW29-5,6) (default 5-ON, 6-ON)

Sub-channels are selectable for switch on DATA or switch on an active interface control lead in groups of 4 Sub-Channels. Set SW29 pos 5 to **OFF** if you want Sub-channels 1 thru 4 set to switch on "Data Transitions". Set pos 5 to **ON** if you want them to be set to switch on "Interface Lead". SW29 pos 6 is associated with sub-channels 5 thru 8.

### Internal Baud Rates (SW30-1,2,3,4)

Switch SW30-1, 2, 3 and 4 select the master clock rate when Internal Clock Source is selected on TX Clock Source or RX Clock Source. In addition the fallback clock rate is also selected with this option. When fallback clocking is required, the RX Clock Source and TX Clock Source should **NOT** be set to **INTERNAL**. Selecting CHAN 6 RXC allows fallback clocks to originate from Sub-Channel 6 pin 17. The following table shows the available rates. If CHAN 6 RXC is selected, SW30-4 can be in either position.

SW30-1	SW30-2	SW30-3	Pos 4=ON	Pos 4=OFF
ON	ON	ON	76800	57600
OFF	ON	ON	38400	28800
ON	OFF	ON	19200	14400
OFF	OFF	ON	9600	7200
ON	ON	OFF	4800	3600
OFF	ON	OFF	2400	1800
ON	OFF	OFF	1200	900
OFF	OFF	OFF	CHAN 6 RXC	



**Fallback Clock Enable (SW-30-6) (default ON)**

If Clock Fallback is required when the primary clock fails, set switch SW30-6 to **OFF**. If Fallback is not required set SW30-6 to **ON**. The Fallback clock rate is selected with the internal baud rate option. When enabled, the Front Panel FBC LED will light if clock Fallback occurs.

**Fallback Clocking From Sub-Channel 6 (SW30-1,2,3)**

To use the Sub-Channel 6 RX Clock as the fallback clock set SW30-1, 2 and 3 to **OFF**. Any other position will select some internal clock rate as called out in the Internal Baud Rates section. **Only Sub-channel 6 can be used as the fallback clock.**

**Fallback Mode Selection (SW30-5) (default ON)**


If Fallback is required, two modes are available via switch SW30-5. To fallback when the transitions from the primary clock source stop, set SW30-5 to **ON**. To fallback when DCD on Sub-Channel 1 becomes inactive, set SW30-5 to **OFF**.

**NOTE: Both TX and RX Clock Source Select MUST Be Set !!!!**

**TX Clock Source Selection (SW27--1,2,3)**

TX Clock Source is selected by switch SW27-1, 2 and 3. The following table indicates the source options provided by the 3080/V24 (CTS DSD-8).

SW27-1	SW27-2	SW27-3	Source
ON	ON	ON	Master
OFF	ON	ON	Port 1
ON	OFF	ON	Port 2
OFF	OFF	ON	Port 3
ON	ON	OFF	Port 4
OFF	ON	OFF	Port 5
ON	OFF	OFF	Port 6
OFF	OFF	OFF	Internal



**TX Clock Pin Selection (SW27-4) (default ON)**

The primary transmit clock can be derived from either pin 15 or 24. If pin 15 is to be the source for primary TX Clock, set SW27-4 to **ON**. If pin 24 is to be the source for primary TX and RX Clock, set SW27-4 to **OFF**.

## RX Clock Source Selection (SW28-1,2,3)

RX Clock Source is selected by SW28-1, 2 and 3. The following table indicates the source options provided by the 3080/V24 (CTS DSD-8). **NOTE:** If TXC Source is pin 24 That clock will be used as the RXC also.

SW28-1	SW28-2	SW28-3	Source
ON	ON	ON	Master
OFF	ON	ON	Port 1
ON	OFF	ON	Port 2
OFF	OFF	ON	Port 3
ON	ON	OFF	Port 4
OFF	ON	OFF	Port 5
ON	OFF	OFF	Port 6
OFF	OFF	OFF	Internal

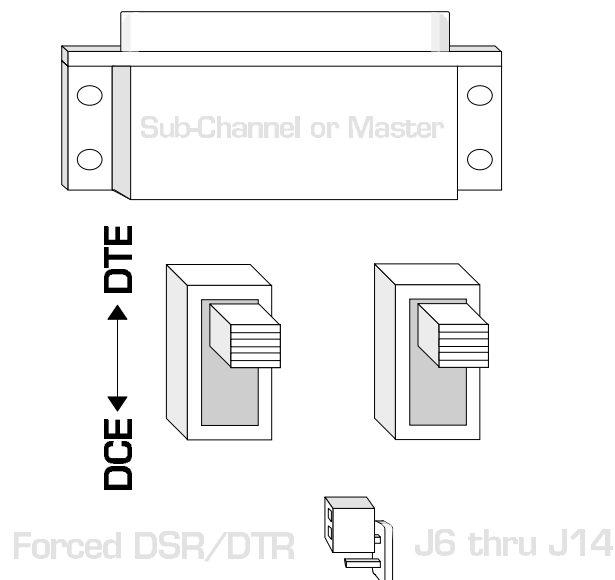


## Tail Circuit Buffer (SW31-1) (default ON)

The 3080/V24 (CTS DSD-8) has a Tail Circuit Buffer that is automatically selected when a Sub-Channel is set as a DTE. The buffer will provide clock synchronization of the data from the Sub-Channel to the Master Port for Tail Circuit operations. The buffer is de-activated when the Sub-Channel is configured as a DCE. When operating in an asynchronous environment the Buffer must be bypassed. To bypass the buffer in a synchronous or asynchronous application, set switch SW31-1 to **OFF** (ASYNC). For normal operation in a synchronous environment set SW31-1 to **ON** (SYNC). When using a tail circuit modem with the 3080/V24 (CTS DSD-8), the modem connected to the Sub-Channel, should be set to External Transmit Clocking. The modem at the remote end of the connection should be set to RX-TX Clocking or Slaved TX Clock. This will insure the same clock is present throughout the network and clock slippage will not occur.

## Port DCE/DTE Selection (SW9 thru SW26)

Slide switches SW9 through SW26 are used to configure DTE/DCE for each port. Slide **Both** switches associated with a port to the same position. *Example:* If connecting a modem (DCE Device) to the PORT, then the port should be configured as a DTE interface. Slide both switches to the DTE position toward the interface connector. If connecting a terminal (DTE) to the Sub-Channel, the port should be configured as a DCE. Slide both switched to the DCE position away from the interface connector. *The port is always configured opposite to the interface that is to be connected to it.* When a port is selected as a DCE, pin 9 and 10 test voltages are provided to the interface connector if JP4 and JP5 are installed. If the port is configured as a DTE, pins 9 and 10 are open. JP4 and JP5 control Plus Unreg and Minus Unreg to all Sub-Channels, therefore it is not possible to have test power on one DCE without having it on all DCE Sub-Channels.



## Test Voltage Enable Pins 9 & 10 (JP4 & JP5)

Pin 9 and Pin 10 unregulated power is provided on each DB-25 connector configured as a DCE if JP4 and JP5 are installed. Pin 9 and 10 unregulated power is not provided to any connector configured as a DTE regardless of JP4 and JP5 installation.

## DTR / DSR Forced Active (JP6 thru JP14)

DTR is tied to DSR on each of the Sub-Channel ports. If the device connected does not supply a DTR but requires DSR (DTE) or does not supply DSR but requires DTR (DCE), installing the forced DTR/DSR jumper (JP6-JP14) for the appropriate Sub-Channel will solve this problem. The line is forced to +6V via an 100Ω resistor, providing isolation to any driver that might be on the interface.

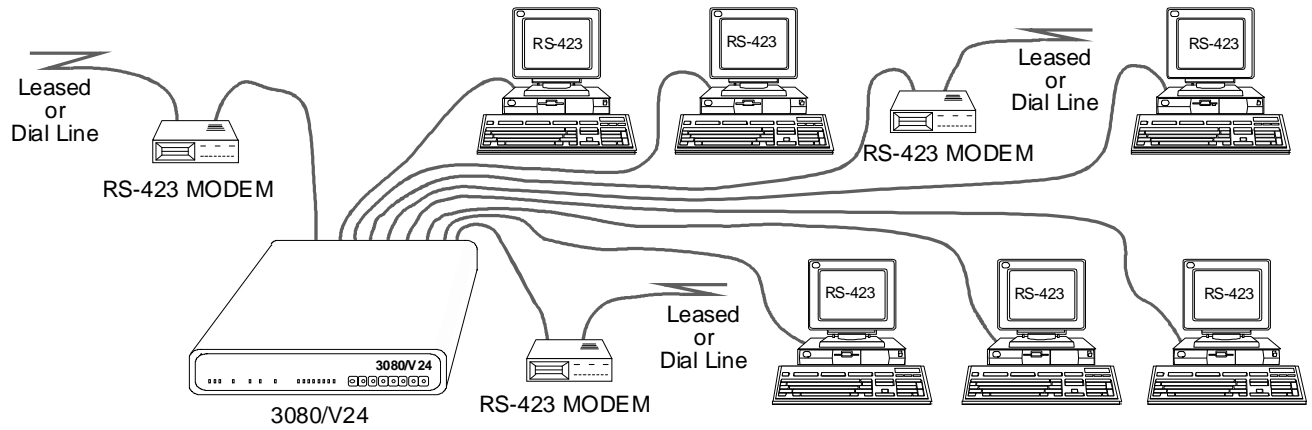
Channel	Jumper	Channel	Jumper
Master	JP6	5	JP11
1	JP7	6	JP12
2	JP8	7	JP13
3	JP9	8	JP14
4	JP10		

## FACTORY Test Jumpers (JP1, JP2 and JP3)

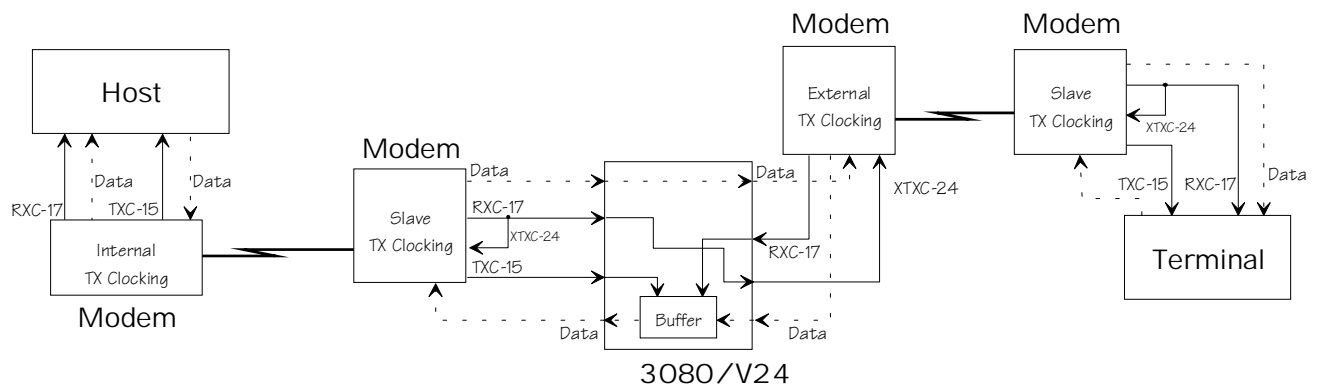
The three factory test jumpers JP1, 2 and 3, must be installed for the unit to properly function. These jumpers are used in the manufacture and test of the product prior to shipment.



# APPENDIX

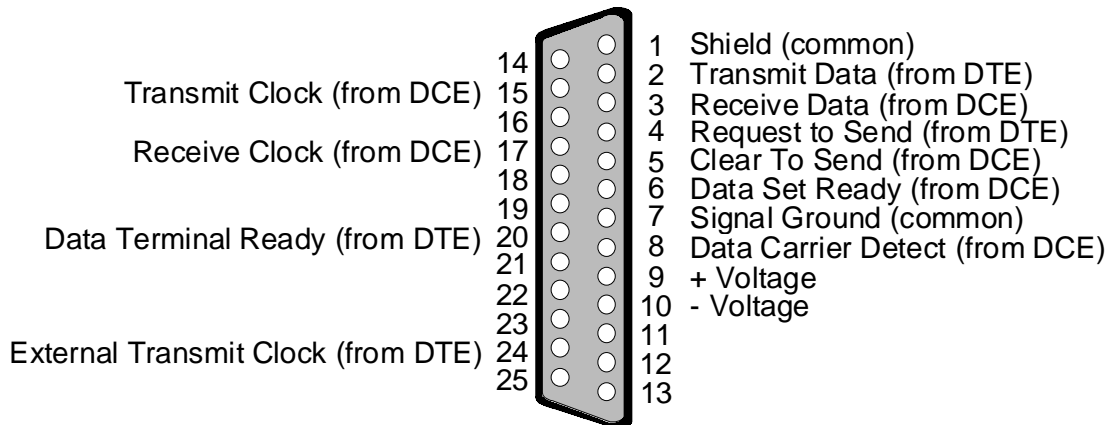


**Typical Application**

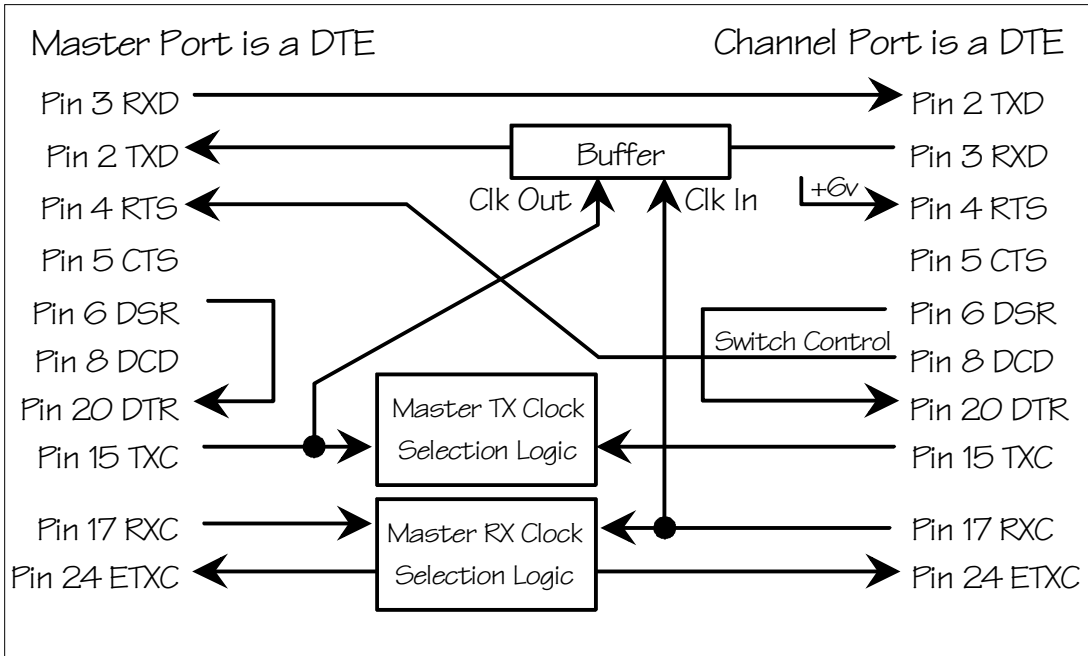


**Typical Tail Circuit Configuration**

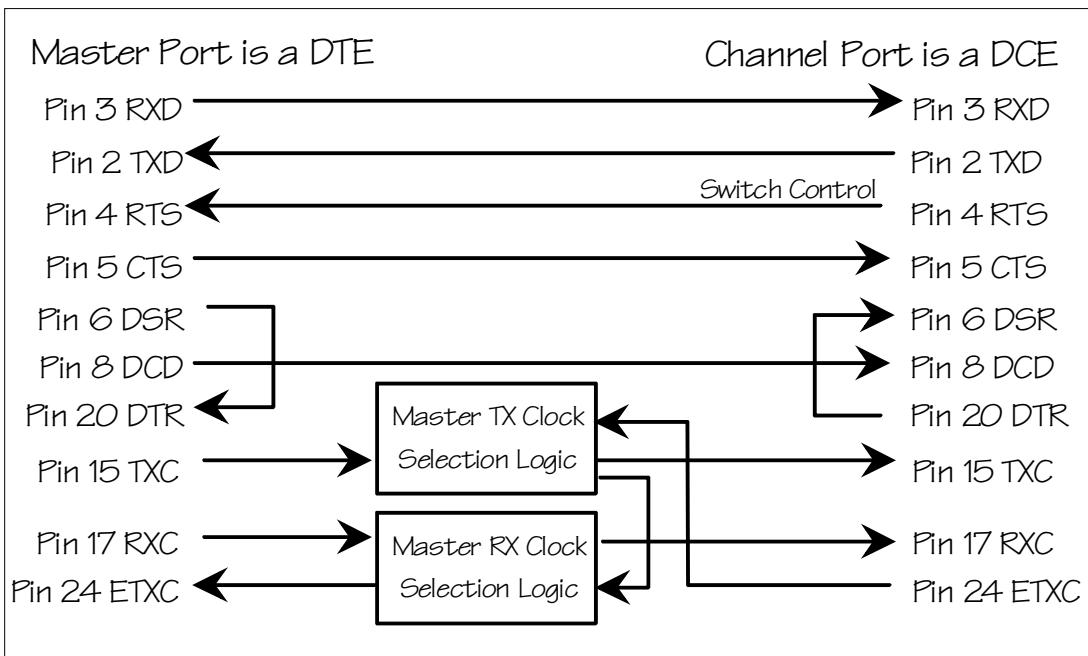
## DB-25 RS-232



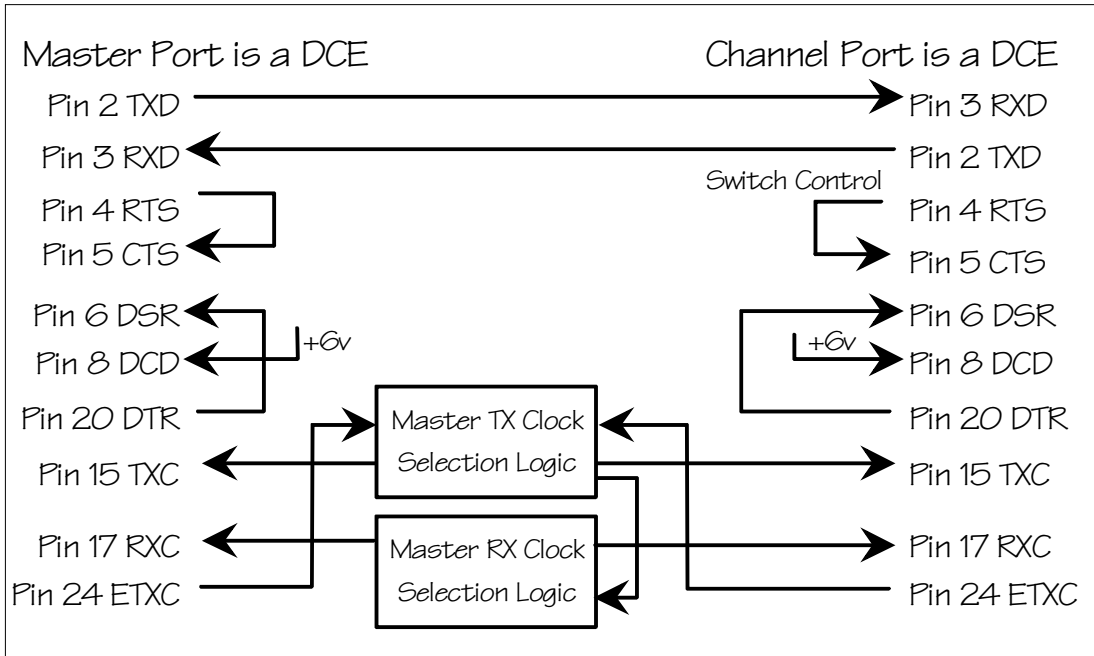
**Interface Pins Supported**



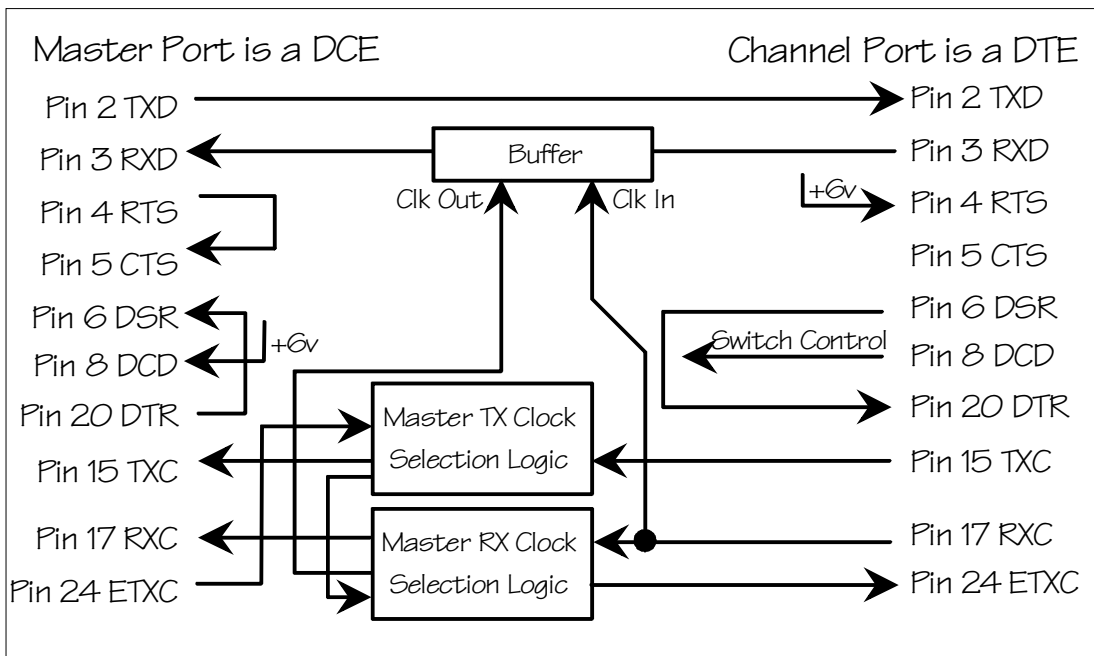
**Master DTE / Sub-Channel DTE Interface Flow Diagram**



**Master DTE / Sub-Channel DCE Interface Flow Diagram**



**Master DCE / Sub-Channel DCE Interface Flow Diagram**



**Master DCE / Sub-Channel DTE Interface Flow Diagram**

# TECHNICAL SPECIFICATIONS

## Applications

Multiple Sync or Async DCE or DTE devices sharing one DCE or DTE link

## Capacity

One to Eight Sync or Async DTE or DCE devices One DCE or DTE Master Channel

## Data Format

Data transparent at all data rates

## Data Rates

Up to 76,800bps

## Timing

Internal: ..... DIP switch selectable  
 Normal: ..... From Modem  
 External: ..... Clock provided on Sub-Channels  
 1-6

## Anti-streaming

Automatic: Selectable timeout intervals or disable

## Terminal Service Modes

Scanning Mode: ..... Channels are continuously scanned for activity on a sequential basis.  
 Priority Mode: ..... Channels are simultaneously monitored channel one has highest access

## Sub-Channel Interface

EIA RS-232 female connectors (DB-25) using RS-423 electrical characteristics.

## Modem Interface

EIA RS-232 female connector (DB-25) using RS-423 electrical characteristics.

## Front Panel

Indicators: ..... Power, Send/Receive Data, Clock Fallback, Sub-Channel Active, Sub-Channel Stream  
 Switches: ..... Enable/Disable each Sub-Channel

## Power Source

100-120/200-240 Vac, 50 to 60 Hz, 0.16/0.08 A, Switch Selectable, 7 Watts

## Certifications

MET, c-MET, CE

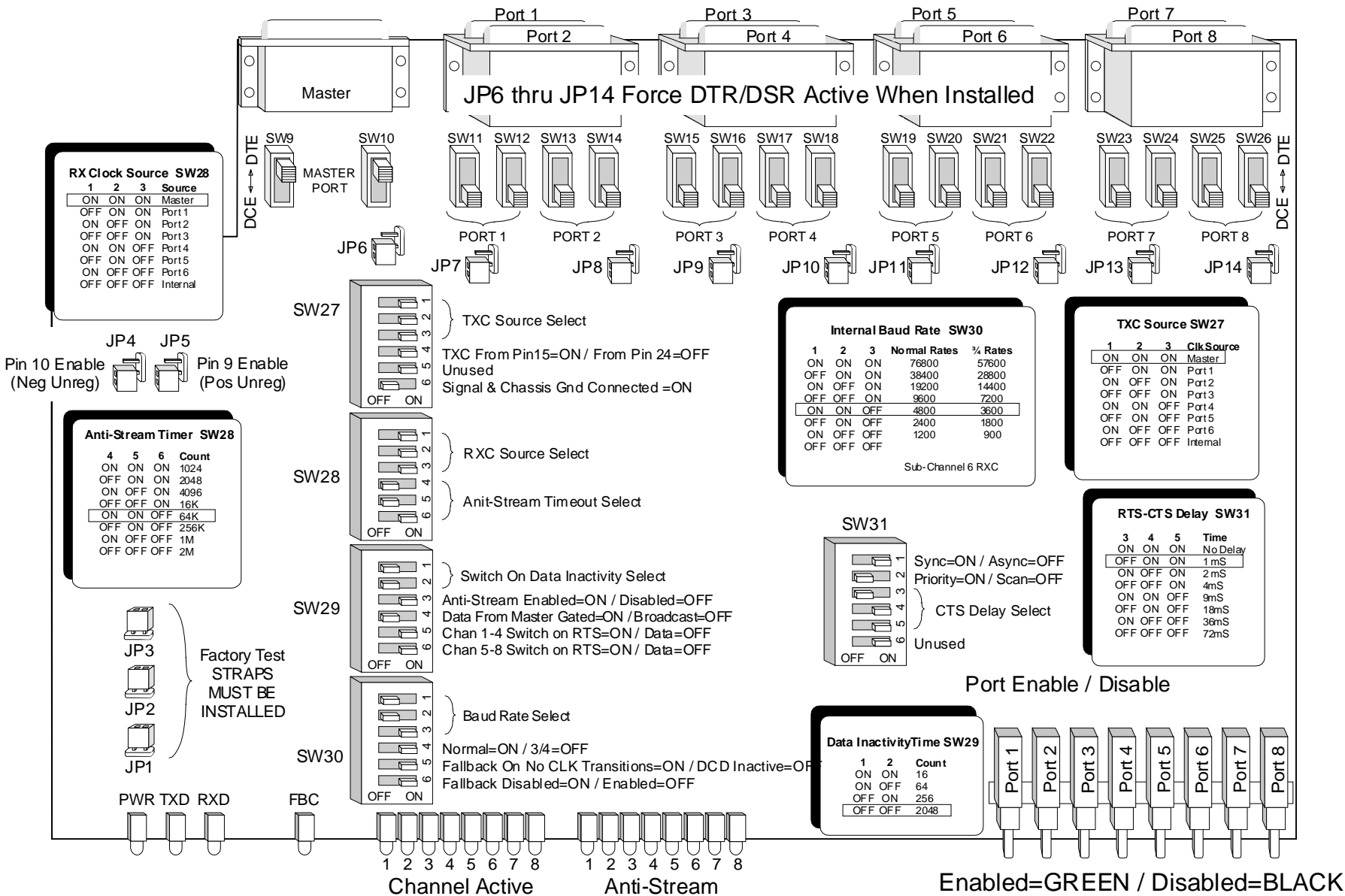
## Dimensions

Height: ..... 1.75 inches (4.44 cm)  
 Width: ..... 17.00 inches (43.18 cm)  
 Length: ..... 11.00 inches (18.93 cm)

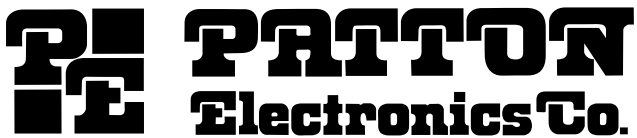
## Weight

4.5 lbs (2.1 Kg)

A-5



APPENDIX



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